



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Hirohisa NAKAYAMA

Application No.: 10/615,846

Docket No.: 116518

Filed: July 10, 2003

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE
SAME, CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

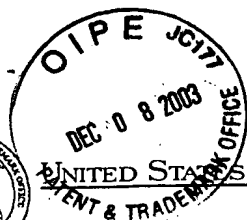
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Date: December 8, 2003

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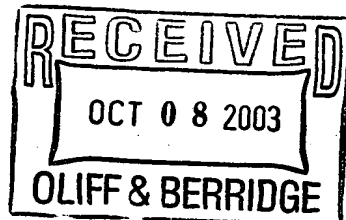


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/615,846	07/10/2003	2812	834	116518	9	20	4

25944
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CONFIRMATION NO. 9984

FILING RECEIPT



OC000000010983093

Date Mailed: 10/06/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

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Assignment For Published Patent Application
Seiko Epson Corporation, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications
JAPAN 2002-216657 07/25/2002

If Required, Foreign Filing License Granted: 10/03/2003

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device and method of manufacturing the same, circuit board, and electronic instrument

Pr liminary Class

438

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Titl 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15

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OLIFF & BERRIDGE, PLC

ATTORNEYS AT LAW

Supplemental Application Data Sheet

Application Information

Application Type:: Regular
Subject Matter:: Utility
CD-ROM or CD-R: None
Title:: SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THE SAME, CIRCUIT
BOARD, AND ELECTRONIC INSTRUMENT
Attorney Docket Number:: 116518
Total Drawing Sheets:: 9
Small Entity:: No

Applicant Information

Applicant Authority type:: Inventor
Primary Citizenship Country:: Japan
Status:: Full Capacity
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Family Name:: NAKAYAMA
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Country of Residence:: Japan

Correspondence Information

Correspondence Customer Number:: 25944

Foreign Priority Information			
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	2002-216657	07/25/2002	Yes
Assignee Information			
Assignee Name::	Seiko Epson Corporation		
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